

1/16

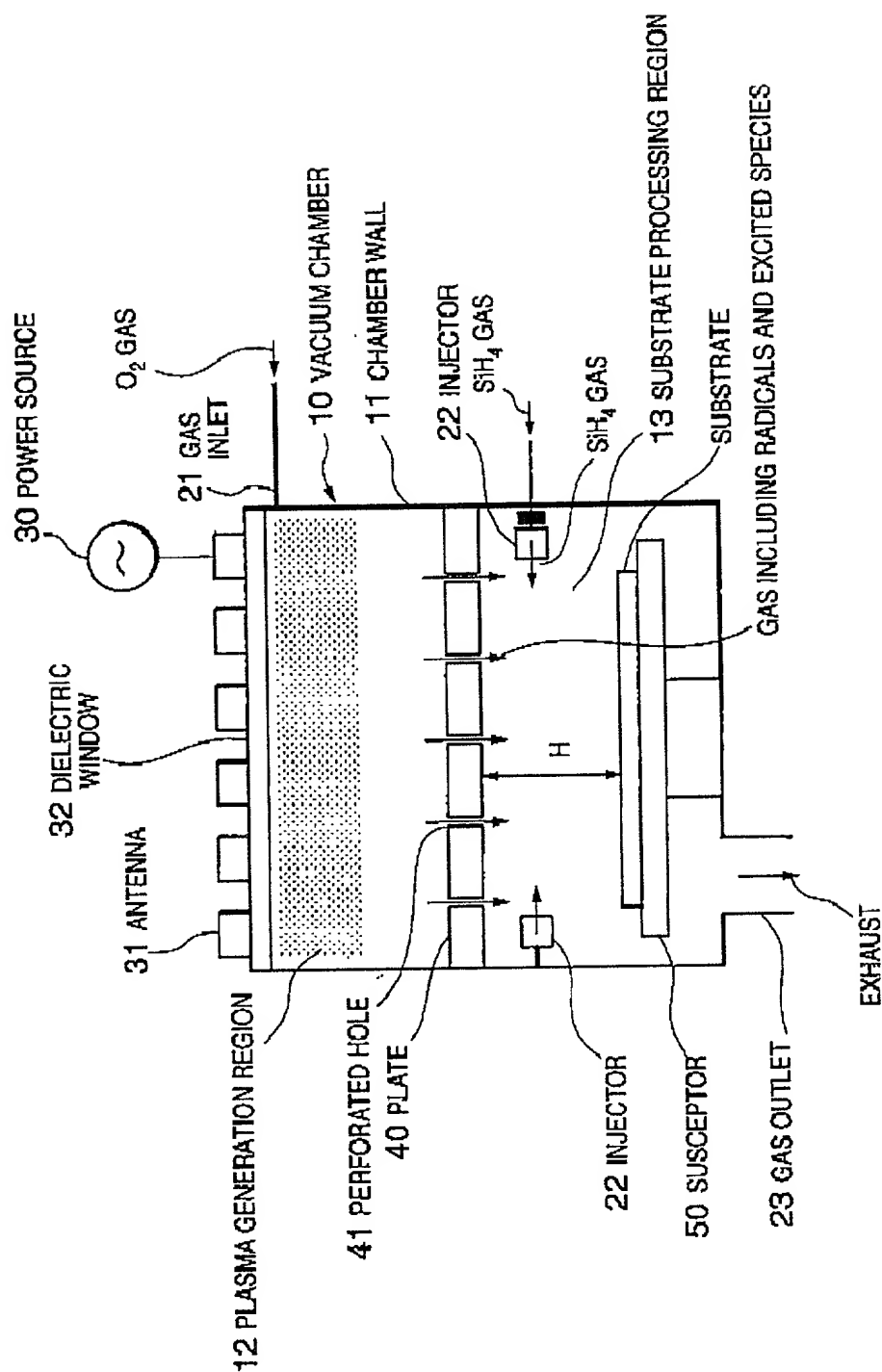


FIG. 1

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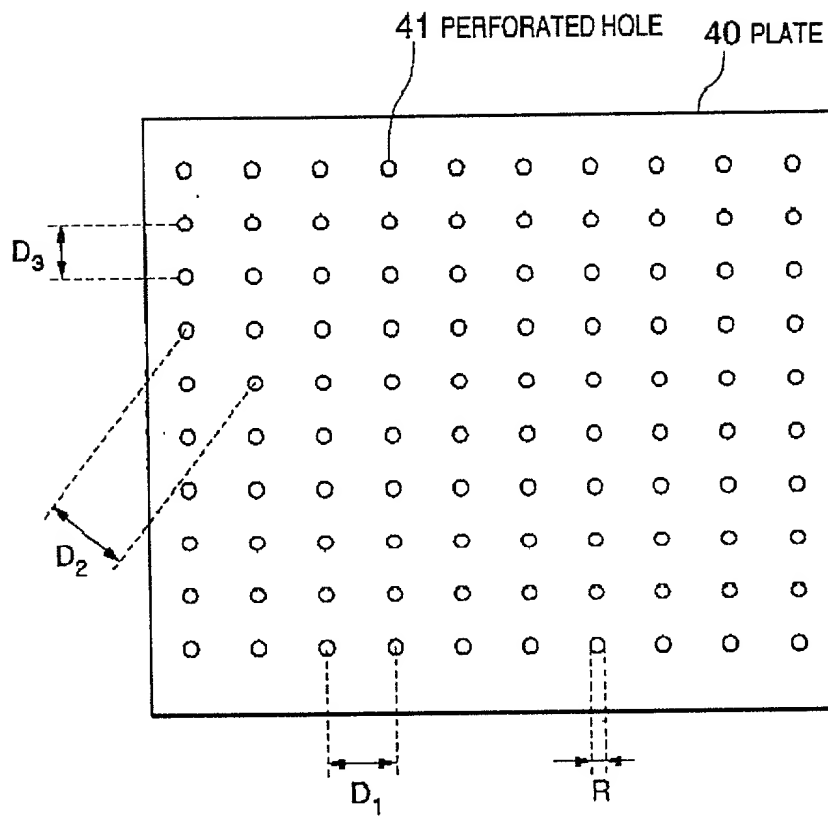


FIG. 2

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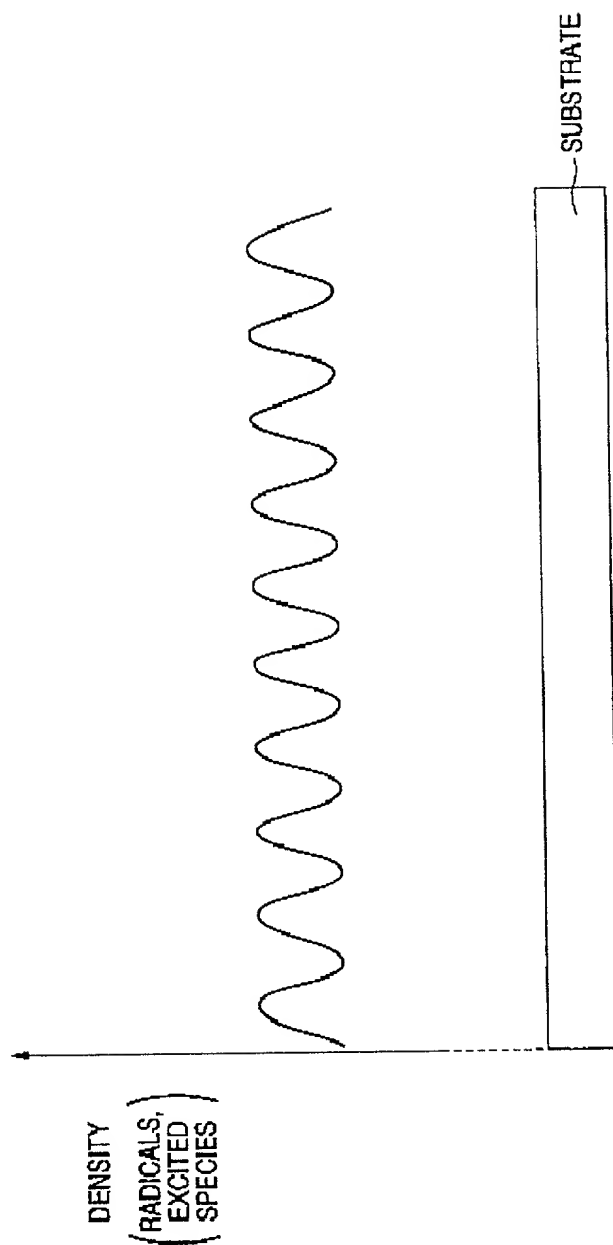


FIG. 3

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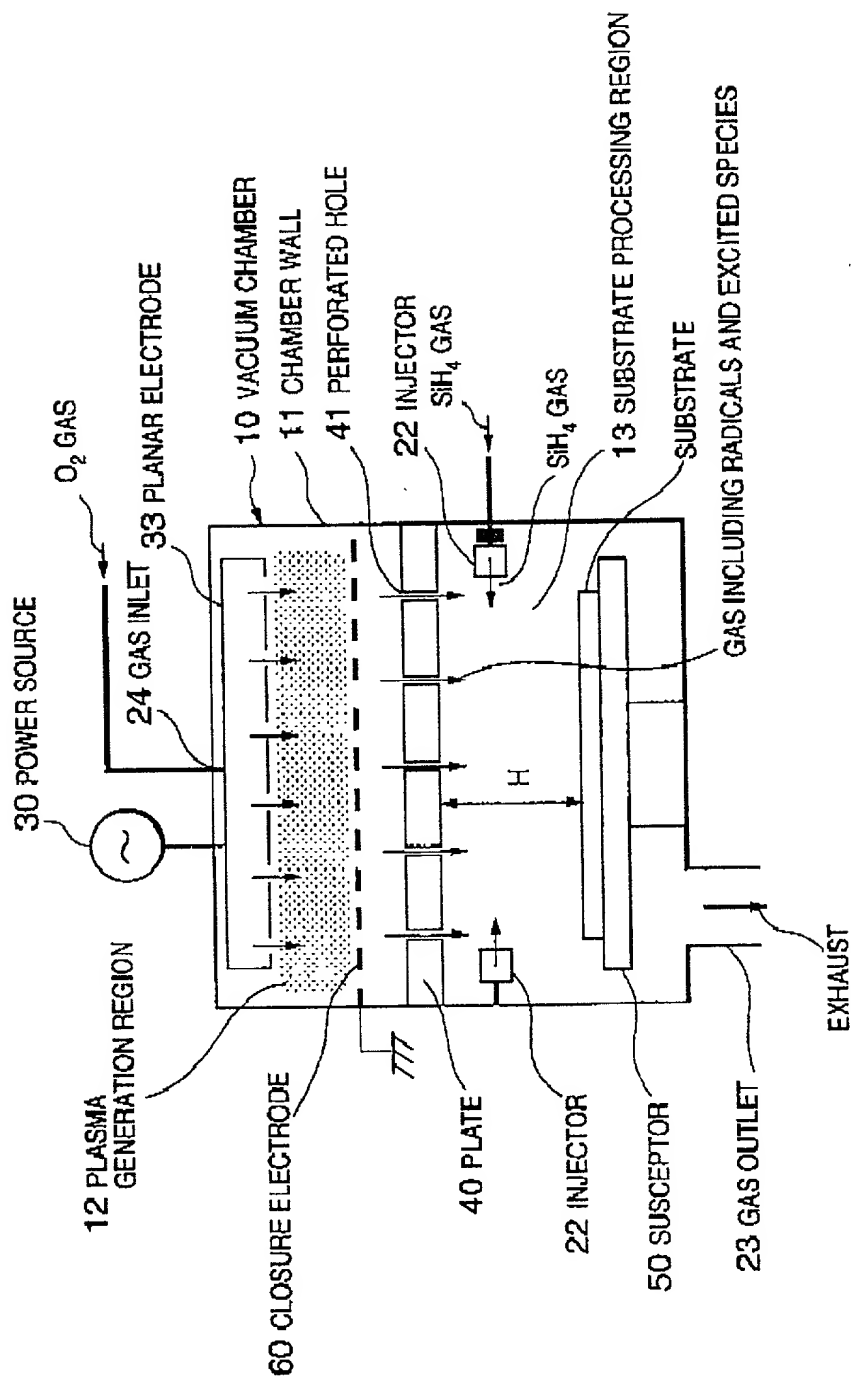


FIG. 4

FIG. 4 is a schematic diagram of a plasma processing system.

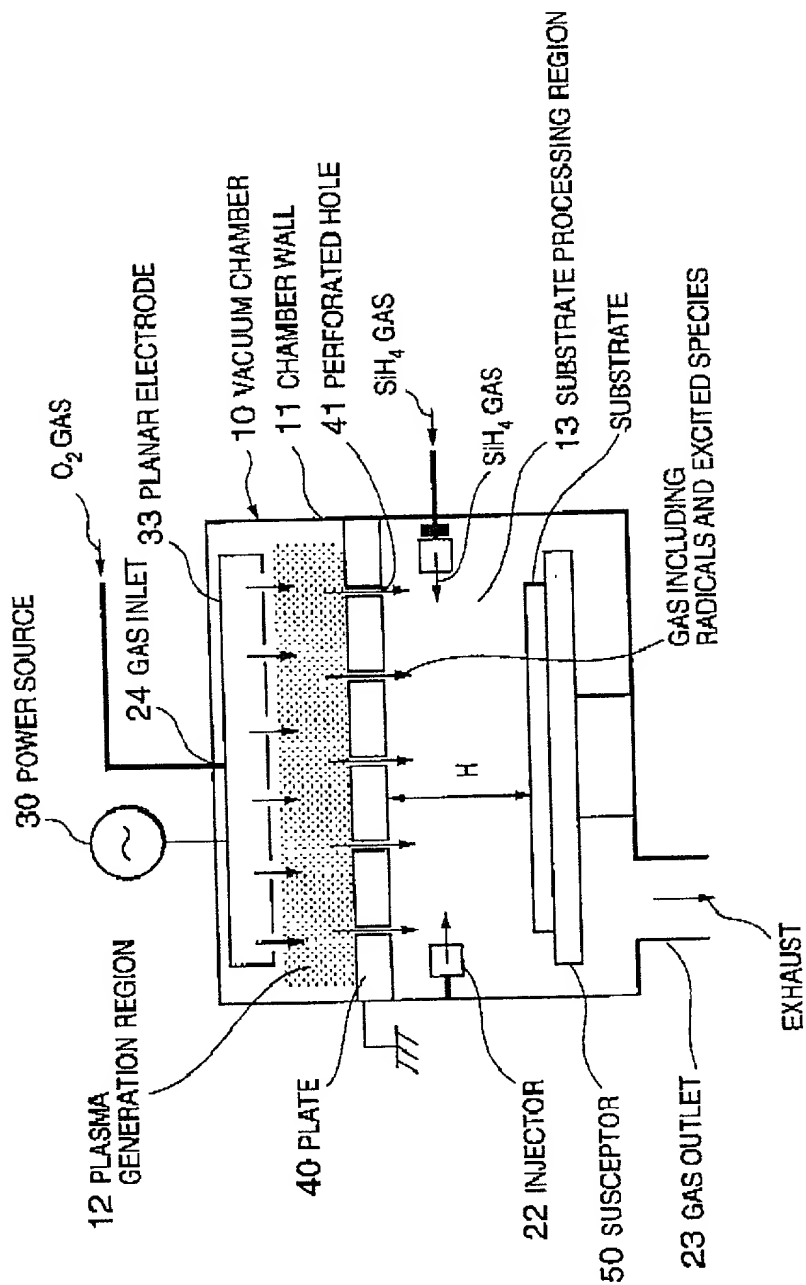


FIG. 5

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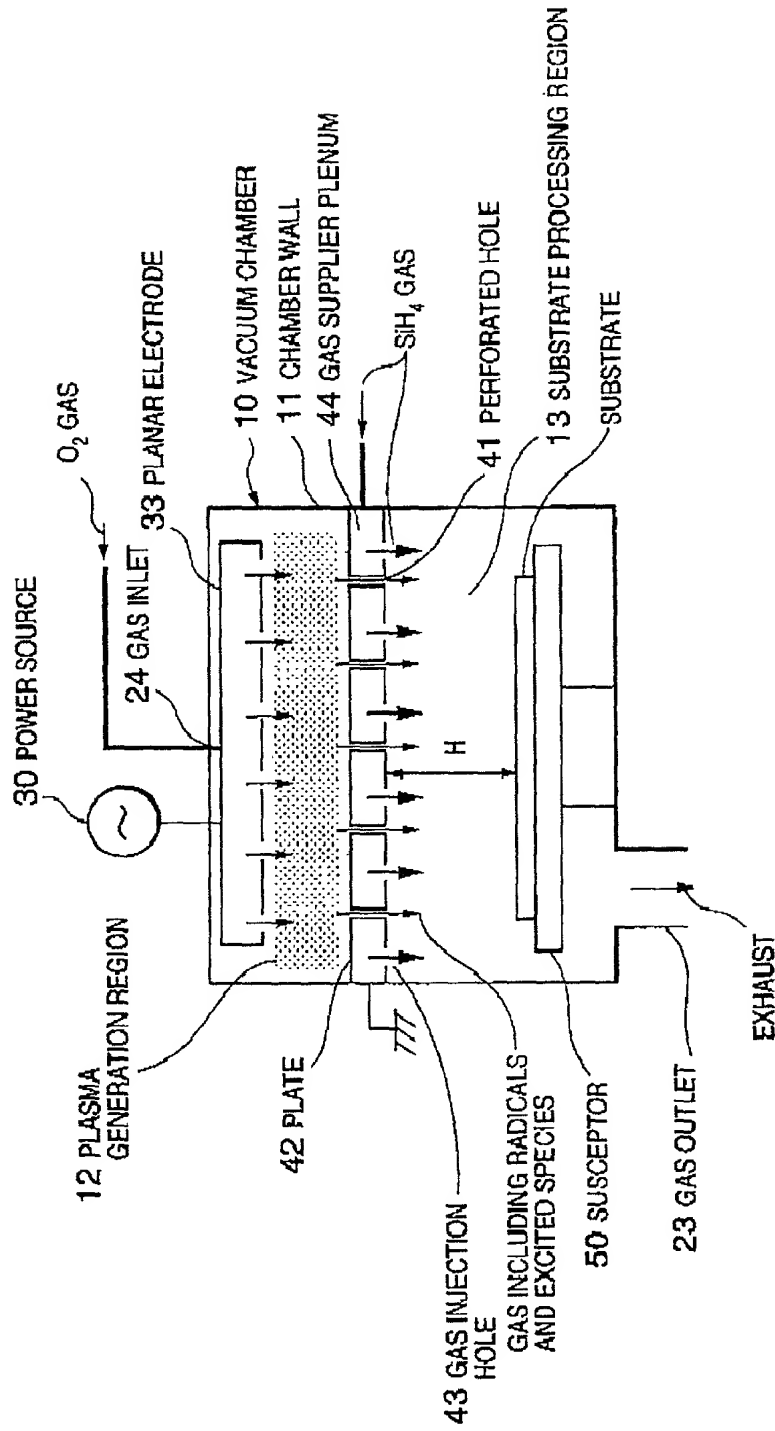


FIG. 6

T O E 3 5 0 3 0 2 5 0

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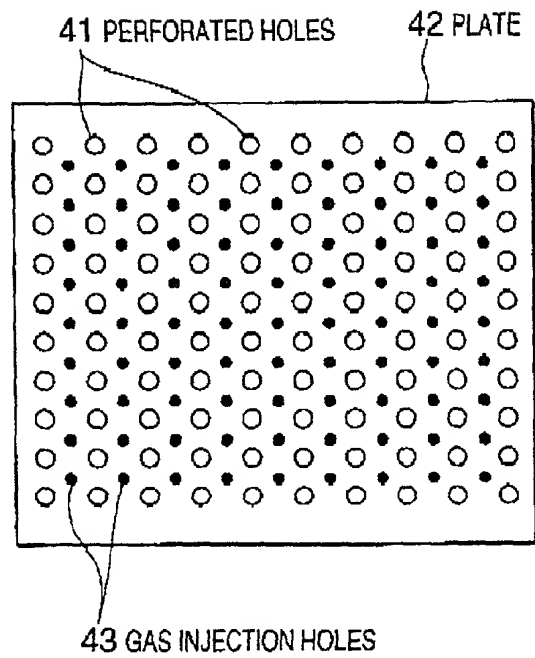


FIG. 7

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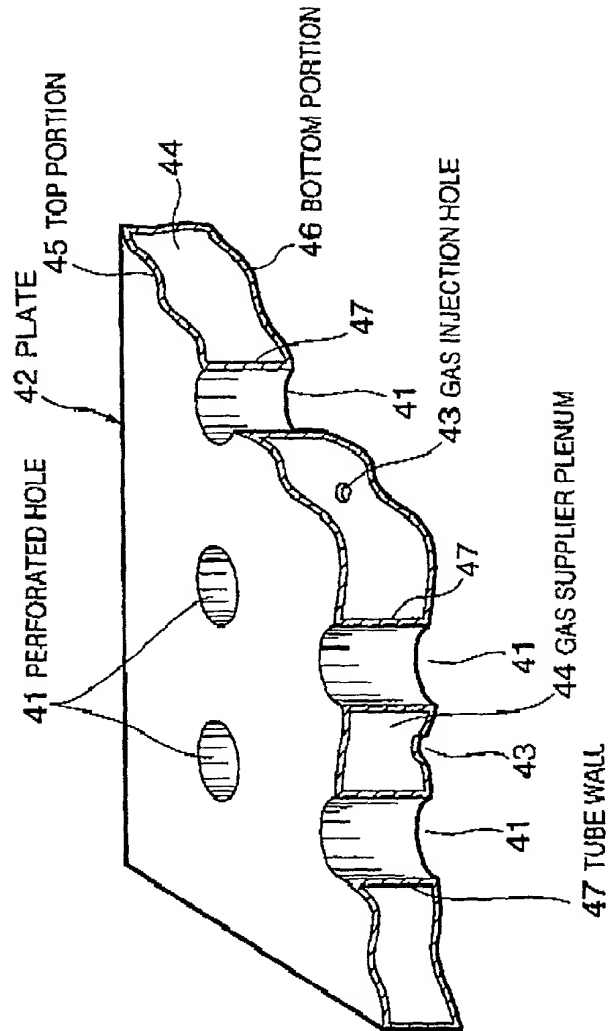


FIG. 8



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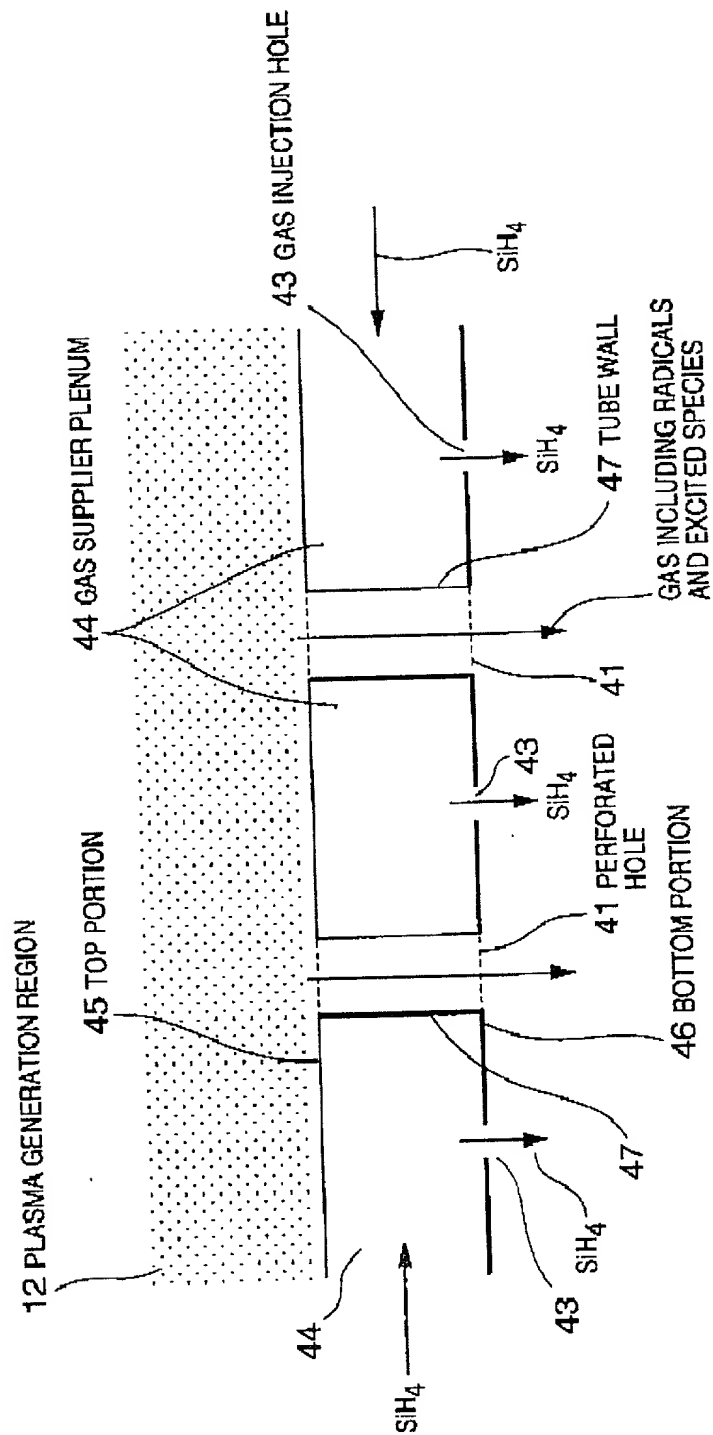


FIG. 9

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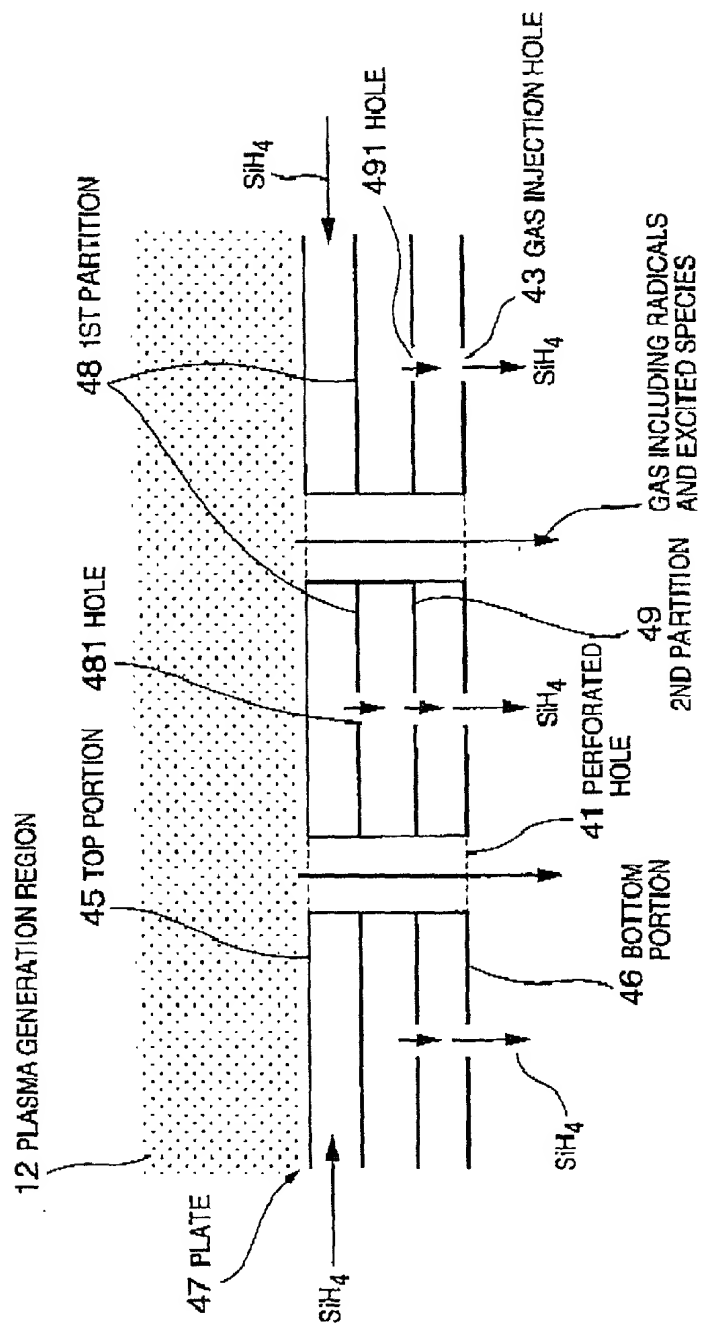


FIG. 10

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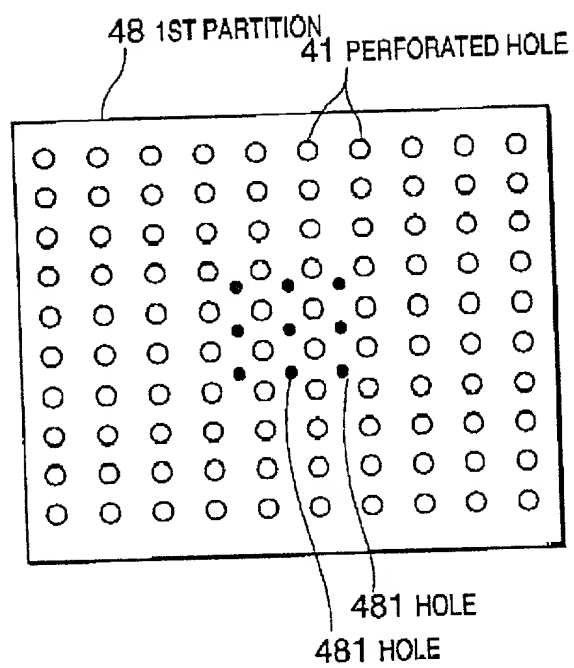


FIG. 11

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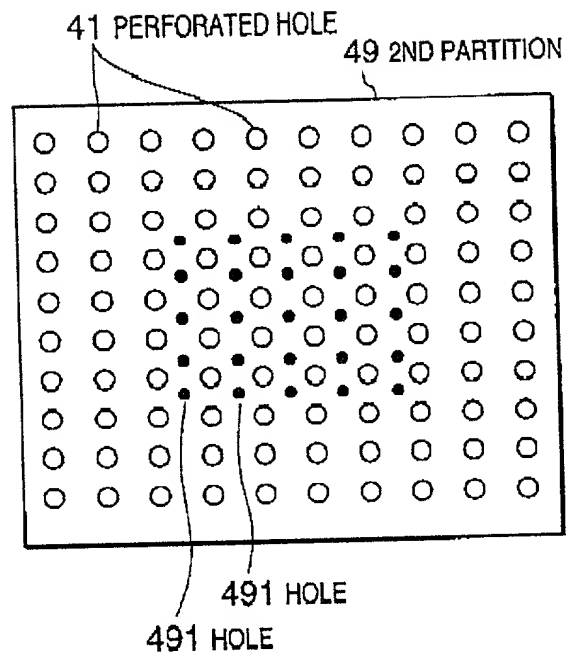


FIG. 12



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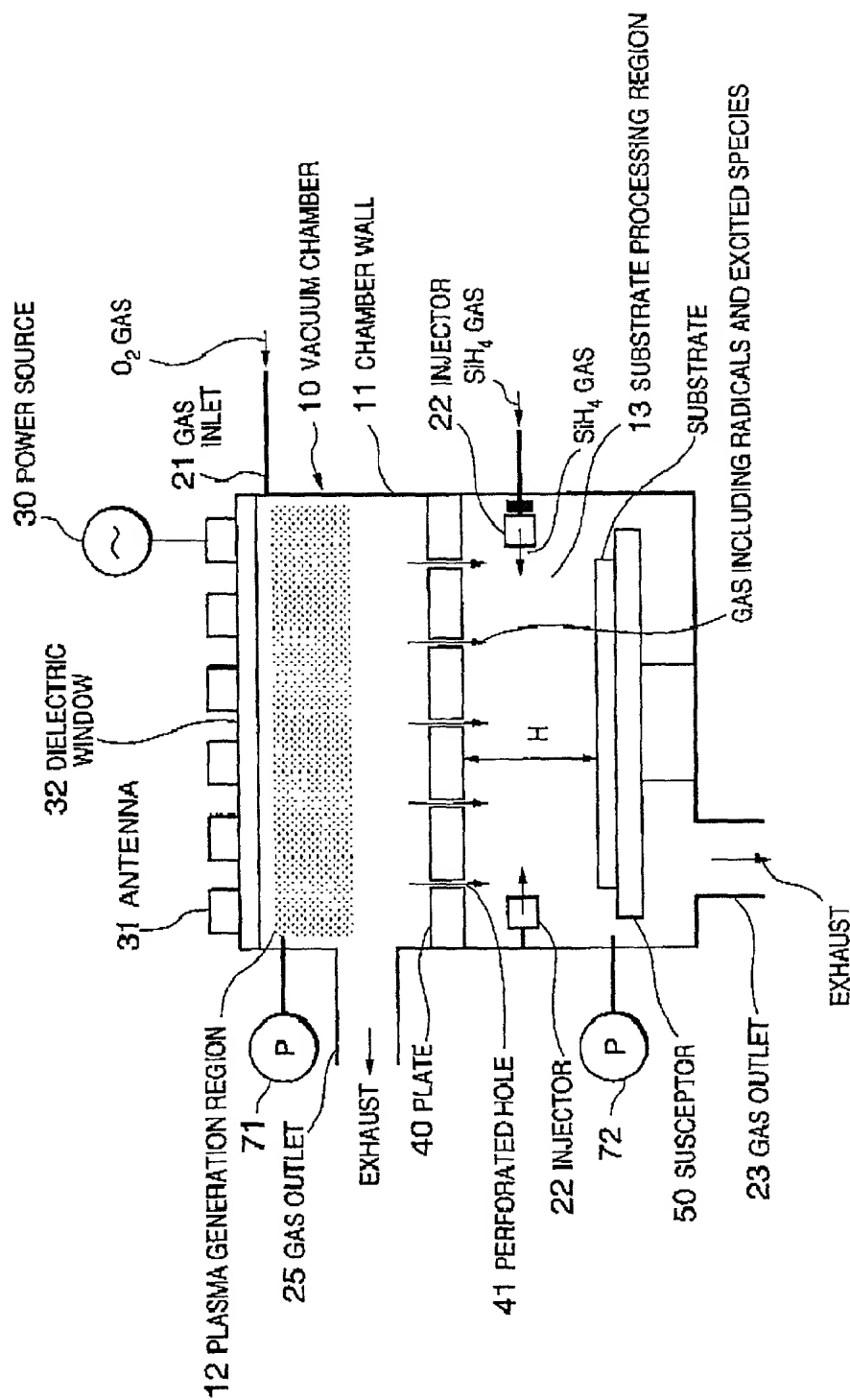


FIG. 14

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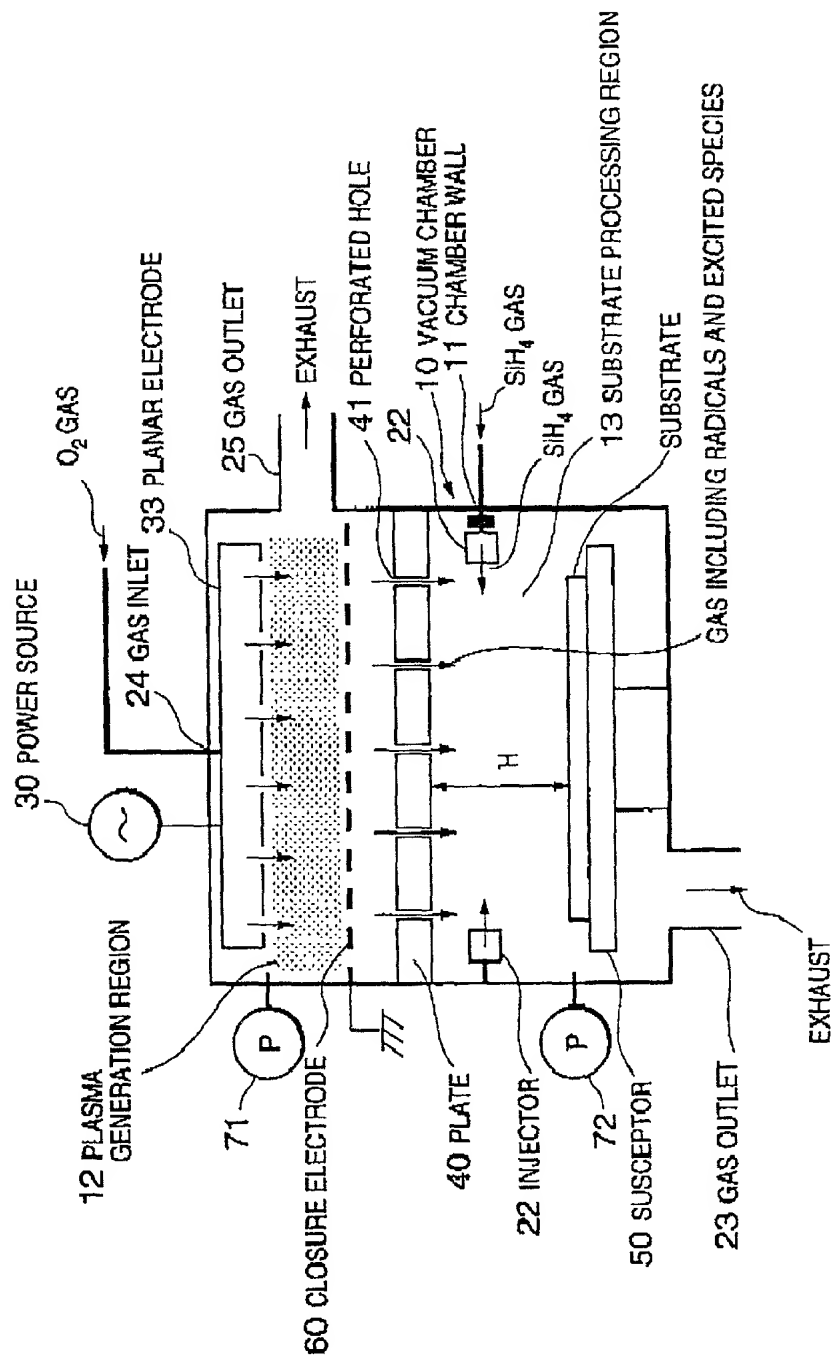


FIG. 15

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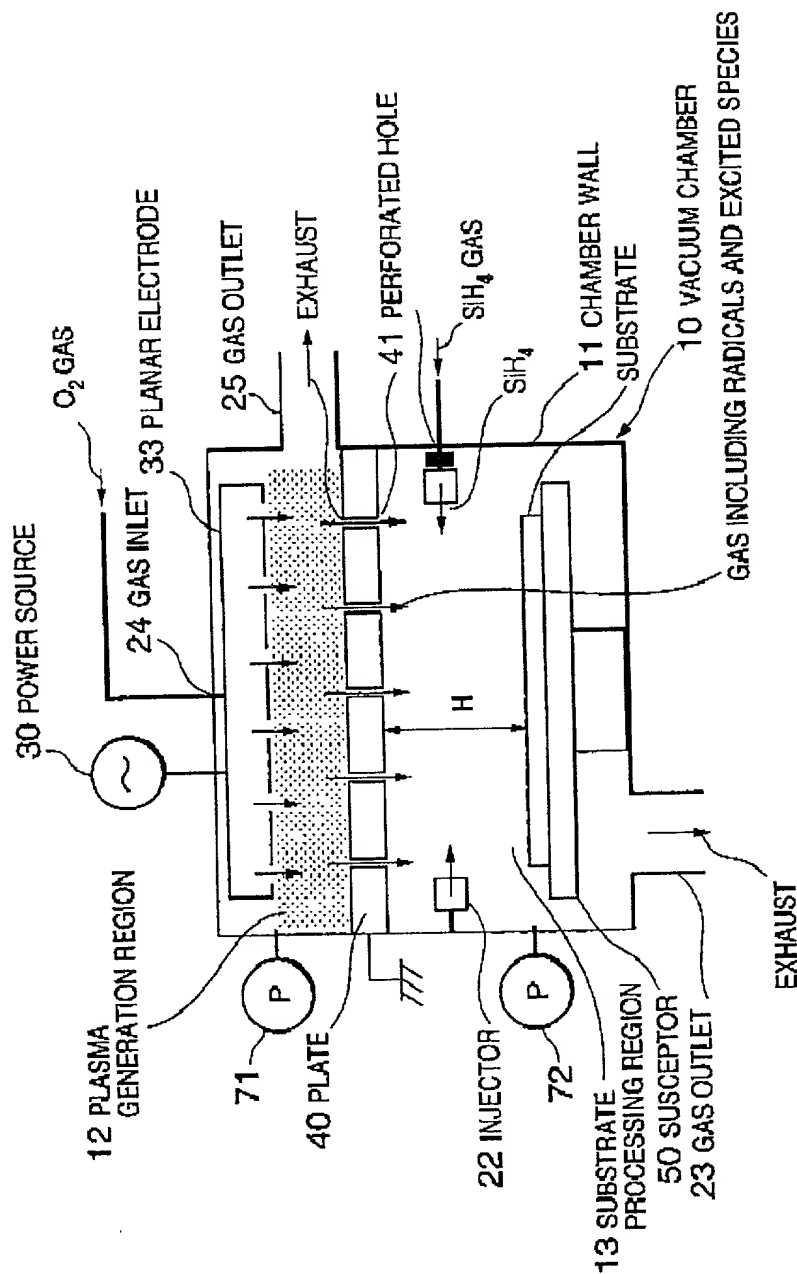


FIG. 16